

Notice of References Cited	Application/Control No. 10/692,582		Applicant(s)/Patent Under Reexamination STAE LIN ET AL.	
	Examiner Christopher L. Lavin		Art Unit 2624	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-7,092,573	08-2006	Luo et al.	382/228
*	C	US-6,119,083	09-2000	Hollier et al.	704/243
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	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

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	U	Huang et al. "Adaptive MLP post-processing for block-based coded images"; Y.-L. Huang and R.-F. Chang; IEE Proc.-Vis. Image Signal Process, Vol. 147, No. 5, October 2000
	V	Kwon et al. "Blocking Artifacts Reduction Algorithm in Block Boundary Area Using Neural Network"; Kee-Koo Kwon, Suk-Hwan Lee, Jong-Won Lee, Seong-Won Ban, Kyung-Nam Park, and Kuhn-Il Lee; Industrial Electronics, 2001. Proceedings. ISIE 2001. IEEE International Symposium on; 2001
	W	Staelin et al. "Neural Network Image Scaling Using Spatial Errors", Carl Staelin, Darryl Greig, Mani Fischer, and Ron Maurer; Oct. 30, 2003
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.